

Amendments to the Specification:

*Please amend the paragraph beginning on page 2, line 20, as follows:*

Referring to FIG. 2, co-processing system 14 includes a high-end processor 20 and a low-end processor 30 connected by a communications bus 25. High-end processor [[22]] 20 includes a quad-data-rate (QDR) interface 22 and a media-switch-fabric (MSF) interface 24.

*Please amended the paragraph beginning on page 7, line 21, as follows:*

A low-end processor 40 is connected to low-end processor 50 by connecting a communications bus [[134]] 135 from QDR interface 42 to a MSF interface 54 of low-end processor 50. In particular, read port 43a is connected to transmit port 57b and write port 43b is connected to receive port 57a.